

IN THE SPECIFICATION:

Please replace the Title of the Invention with the following amended Title:

--ORGANIC ELECTROLUMINESCENT DISPLAY DEVICE WITH INSULATING
LAYER PATTERNS AND METHOD OF FABRICATING THE SAME--

Please replace paragraph [0012] with the following amended paragraph:

-- FIG. 4 is a cross sectional view along IV-IV of FIG. 3 according to the related art. In FIG. 4, the OELD device includes the driving TFT T_D , a first electrode 36, a light-emitting layer 38, and a second electrode 80, wherein the driving TFT T_D has a gate electrode 68, an active layer 62, a source electrode 66, and a drain electrode 63. Accordingly, the first electrode 36 is formed over the driving TFT T_D and is connected to the drain electrode 63 of the driving TFT T_D with an insulating layer 67 between the first electrode 36 and the driving TFT T_D . The light-emitting layer 38 is formed on the first electrode 36 for emitting light of a particular color wavelength within an emission region E, and the second electrode 80 is formed on the light-emitting layer 38. A storage capacitor C_{ST} (in FIG. 3) is connected in parallel to the driving TFT T_D , and includes first and second capacitor electrodes 35 and 55. The source electrode 66 of the driving TFT T_D contacts the second capacitor electrode 55, i.e., a power line, and the first capacitor electrode 35 is formed of polycrystalline silicon material under the second capacitor electrode 55. The second electrode 80 is formed on the substrate 32 on which the driving TFT T_D , the storage capacitor C_{ST} , and the organic light-emitting layer 38 are formed. Accordingly, the OELD device is a bottom emission-type OELD device, wherein the light-emitting layer emits the light downward through the substrate 32. In addition, each of the pixels having the driving TFT T_D and the storage capacitor C_{ST} are separated by partitions formed between two adjacent pixels.--